

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): KAJIHARA, et al.
Serial No.: 09/987,978
Filed: November 16, 2001
For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE USING THE SAME, AND METHOD OF AND
PROCESS FOR FABRICATING THE TWO
Group: 2815
Examiner: Jasmine Jhihan B Clark
Conf. No.: 7905

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

November 28, 2011

Sir:

In response to the Office Action dated September 28, 2011, the remarks are
set forth on the following pages:

Remarks